CLAIM

1.   A copy preventing device comprising:

CPU 1;

an interface 22 for inputting a random value from an outer device 27 as an electric product stored a predetermined random value data, for determining whether it is copied or not;

a ROM table 21 for storing the random value from the interface 22 and storing the constant value for coding;

an operating unit 23 for operating the random value data from the interface 22 and the constant value from the ROM table 21;

a register 24 for storing the operated data from the operating unit 23; and

an outer device 25 for outputting the operated data from the register 24 and for determining whether the product can be operated or not according to the compliance to the coding signal stored in the outer device 27.

2.   The device according to the claim 1, wherein said CPU 1 stores several algorithms so that other algorithms can be applied when the present algorithm is revealed.

3.   The device according to the claim 1, wherein said CPU 1 has a ROM table stored an inner constant value for coding so that a coding operation can be exchanged without exchanging an entire mask.

4.   A coding operating system of ALPU device comprising:

an interface 32 for inputting/outputting the data signal from/to an outer device 31;

a code determining unit 35 for determining the code according to an outer pin option 34 when the most upper code is inputted from the interface 32;

a ROM table 37 for selecting the most upper code according to the outer pin

option 34;

a flash memory or EPROM 36 for exchanging the ROM value according to the determining result of the most upper code determining unit 35;

an operating unit 38 for inputting the data from the EPROM 36 and the data from the ROM table 37 and the random data from the interface 32 for operating the coding; and

a register 39 for outputting the operated value to the interface 32 for outputting the operated value to the outer device 31.

5. A coding operating method of ALPU uses One Time Program or Multiple Time Program for the code exchanging system when the exchanging method is established for exchanging a code and a serial number.

6. A coding operating method of ALPU when the outer pin is established the high level or the low level the most upper code can be exchanged so that an appropriate action can be applied when the most upper code is revealed.

7. A coding operating device comprises:
ALPU RF IC 70 for outputting the radio signal;
RFID Reader 40 for reading the radio signal from the ALPU RF IC 70; and
RFID server 100 for determining whether the originally established signal or not according the radio signal read by the RFID Reader 40.

8. The device according to the claim 7, wherein said ALPU RF IC 70 comprises RFIC 50 for inputting/outputting from/to the RF Reader 40; and ALPU 60 for encoding and decoding the radio signal from the RFID Reader 40.

9. The device according to the claim 7, wherein said ALPU 60 comprises ECC(Error Correction Code) 80 for encoding and decoding for correcting the error data during the input/output between the RFID Reader 40 and RFID and RF IC and ALPU or

for correcting the partial defection of the inner side of the semiconductor.

10.   The coding operating method comprising:

        step that the radio signal from the RF IC 50 is inputted to the ALPU 60 and to
the encoder through ECC 80 for encoding and the output signal from the ALPU 60
decodes by the decoder and outputs to the RF IC 50 through ECC 80 so that RFID
Reader 40 can read the outputted value from the RF IC 50.